

WHAT IS CLAIMED IS:

- Sub C1
1. An active low-pass filter system including:
a low-pass filter circuit including a resistive forward signal flow branch; and
an isolated-integrator band-reject filter coupled to the low-pass filter circuit forward signal flow branch.
 2. The system of Claim 1, wherein the band-reject filter includes a resistor for tuning the band-reject filter.
 - Sub D3
3. The system of Claim 1, wherein the low-pass filter circuit is a Sallen & Key filter.
 4. The system of Claim 1, wherein the low-pass filter circuit is a multiple feedback filter.
 5. The system of Claim 1, wherein the low-pass filter circuit is a state variable filter.
 - Sub 23b
6. A power amplifier system for driving a load comprising:
a pulse width modulation circuit creating ripple spectra;
a feedback control loop coupled to said pulse width modulation circuit and including an active low-pass filter, said low-pass filter including a feedback demodulation filter and an isolated-integrator frequency-rejecting network.
 7. The system of Claim 6, wherein the frequency-rejecting network is an isolated-integrator band-reject filter, including a resistor for tuning the band-reject filter.
 8. The system of Claim 6, further including an error amplifier and modulator circuit connected to an input of the pulse width modulation circuit, and a demodulation filter connected between the pulse width modulation circuit and the load.
- Sub C3